

Call for Papers - extended

Austrochip is an annual workshop on microelectronics and integrated circuits in Austria and neighboring countries. The 30th edition will be organized by the Carinthia Institute for Microelectronics (CIME) of University of Applied Sciences (CUAS) in Villach. Furthermore, tutorial sessions on the subject of integrated power electronics are planned in advance.

Topics of the workshop include but are not limited to:

- Analog, mixed-signal, and RF integrated circuits
- Digital circuits, filters, DSPs, asynchronous designs
- FPGA design and reconfigurable hardware
- Design methodology, system-level design, giga scale circuits, network-on-chip
- Embedded systems and IoT, energy-efficient machine learning, low-power designs, RF systems, security aspects
- Verification and testing, signal integrity, device modeling, timing analysis, reliability simulation, EMC, ESD
- Emerging technologies, quantum computing, nano CMOS process, sub-threshold circuits, sensors, organic and biomedical electronics
- Case studies and prototyping

Submissions from academia as well as industry are encouraged. Especially, PhD students are invited to present their work related to topics of Austrochip, but also excellent MSc theses will be considered. Submissions will be double-blind reviewed. Accepted papers will be made available electronically at the conference and published in IEEE Xplore (indexed by well-known major indexing services).

Instructions for Authors:

Papers should not exceed 4 pages IEEE format (including bibliography and appendix). Instructions and additional details can be found [here](#).

Conference Venue:

Austrochip 2022 is held in Villach on October 11th, 2022 at the Congress Center Villach. As part of the 30th anniversary of Austrochip, tutorial sessions on Integrated Power Electronics and High Level Design and Verification as well as a conference dinner will be offered.

Contact/Organization:

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Conference Venue



Congress Center Villach

Important Dates

Submission Deadline

July 31st, 2022

Acceptance of Notification

September 5th, 2022

Workshop

October 11th, 2022

Tutorial Session 1 & 2

October 10th, 2022 October 12th, 2022

Organizer



Technical co-sponsor



Registration Information

Registration / CfP:

<https://www.conftool.net/austrochip2022>