

Austrochip 2021

29th Austrian Workshop on Microelectronics October 14th, 2021 - Linz, Austria

3rd Call for Papers

Austrochip is an annual workshop on microelectronics and integrated circuits in Austria and neighboring countries. The 29th edition will be organized by the Institute for Integrated Circuits of Johannes Kepler University in Linz.

Topics of the workshop include but are not limited to:

- Analog, mixed-signal, and RF integrated circuits
- Digital circuits, filters, DSPs, asynchronous designs
- FPGA design and reconfigurable hardware
- Design methodology, system-level design, giga scale circuits, network-on-chip
- Embedded systems and IoT, energy-efficient machine learning, low-power designs, RF systems, security aspects
- Verification and testing, signal integrity, device modeling, timing analysis, reliability simulation, EMC, ESD
- Emerging technologies, quantum computing, nano CMOS process, sub-threshold circuits, sensors, organic and biomedical electronics
- · Case studies and prototyping

Submissions from academia as well as industry are encouraged. Especially, PhD students are invited to present their work related to topics of Austrochip, but also excellent MSc theses will be considered. Submissions will be double-blind reviewed. Accepted papers will be made available electronically at the conference and published in IEEE Xplore (indexed by well-known major indexing services).

Instructions for Authors:

Papers should not exceed 4 pages IEEE format (including bibliography and appendix). Instructions and additional details can be found here.

Conference Venue:

Austrochip 2021 is held in Linz on October 14th, 2021 at the Johannes Kepler University. We are currently planning a face-to-face event at the JKU campus, but due to the COVID-19 restrictions, an alternative purely online mode or a hybrid variant is also being considered.

Contact/Organization:

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Conference Venue



Important Dates

Submission Deadline extended: August 1st, 2021

Acceptance Notification September 9th, 2021

Workshop October 14th, 2021

Organizer



IIC – Institute for Integrated
Circuits

Technical co-sponsors





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