

Important Dates

- Submission deadline
July 15, 2013
- Notification of acceptance
September 2, 2013
- Workshop
October 10, 2013

Program Committee

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Thomas Bauernfeind	Intel
Dieter Draxelmayr	Infineon
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Organization



Institute for Integrated Circuits



JKU
JOHANNES KEPLER
UNIVERSITY LINZ

Austrochip 2013

The Austrochip Microelectronics Workshop is a forum for presenting the latest activities in the field of integrated circuits and microelectronics. Austrochip 2013 will be held at the Park Inn Hotel in Linz.

Call for Papers

The Austrochip Microelectronics Workshop focuses on the design of analog, digital and mixed-signal integrated circuits and systems. Submissions should cover, but are not limited to the following topics.

- Integrated analog, digital, mixed mode and RF circuits
- FPGA design and reconfigurable systems
- Design tools and methodologies
- EMC as applied to integrated circuits
- Integrated circuit verification and testing
- Innovative and new technologies (e.g. organic electronics)

Blind submissions are subject to peer review. Student submissions are assessed separately.

Instructions for authors

The official language of the conference is German, but English papers and presentations are welcome. Papers should not exceed 6 pages. Instructions and additional details regarding format can be found at www.austrochip.at.

Location

The 21st Austrochip Microelectronics Workshop will be held at the Park Inn Hotel in Linz on October 10, 2013.

Registered participants will be provided with printed proceedings, lunch and coffee breaks.

Contact

If you have any questions, contact the local organization at:
E-Mail: austrochip2013@riic.at
Phone: +43 732 2468 4738.

