

# **Austrochip 2014**

22<sup>nd</sup> Austrian Workshop on Microelectronics 09 October 2014, Graz, Austria

# **Call for Papers**

Austrochip is an annual workshop on microelectronics and integrated circuits in Austria and neighboring countries. The 22th edition will be held by the Institute for Applied Information Processing and Communications (IAIK) at Graz University of Technology.

Topics of the workshop include but are not limited to:

- Analog, mixed-signal, and RF integrated circuits
- Digital circuits, filters, DSPs, asynchronous designs
- FPGA design and reconfigurable hardware
- Design methodology, system-level design, gigascale circuits, network-on-chip
- Embedded systems, low-power designs, RF systems, security aspects
- Verification and testing, signal integrity, device modelling, timing analysis, reliability simulation
- Emerging technologies, nano CMOS process, sub-threshold circuits, sensors, organic and biomedical electronics
- Case studies and prototyping

Submissions will be blind-reviewed. Accepted papers will be published in printed conference proceedings and electronically through IEEE Xplore (with indexing by well-known major indexing services).

### **Instructions for Authors:**

Papers should not exceed 6 pages (including bibliography and appendix). Instructions and additional details regarding format can be found at <a href="http://www.austrochip.at">http://www.austrochip.at</a>.

#### **Conference Venue:**

The 22<sup>nd</sup> Austrochip Workshop on Microelectronics is held in Graz on the 9th October 2014 at Hotel Weitzer Graz. Registered participants will be provided with printed proceedings, lunch, and coffee breaks.

# Contact/Organization:

If you have any questions, contact the local organization:

Michael Hutter (TU Graz, IAIK), Phone: +43 316 873 5541

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## **Important Dates**

**Submission Deadline** 11 July 2014

Acceptance Notification 04 September 2014

Workshop 09 October 2014





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