

Important Dates

- Submission Deadline
08 July 2012
- Acceptance Notification
10 September 2012
- Workshop
10 October 2012

Organization



Joanneum Research

Call for Papers

The Austrochip workshop deals with the design of analog, digital, and mixed-signal integrated circuits and systems. Submissions should cover, but are not limited to, the following topics:

- Integrated analog, digital, mixed mode and RF Circuits
- FPGA design and reconfigurable systems
- Design tools and methodologies
- EMC regarding integrated circuits
- Verification and testing of integrated circuits
- Innovative and new technologies (e.g. organic electronic)

The peer review will assess blinded submissions. Student submissions are rated separately.

Instructions for the authors

Submissions are accepted in German or English. The official workshop language is German, submissions in English are welcome, too. Articles should not exceed 6 pages. Instructions and further details regarding formatting are available on the website www.austrochip.at.

Location

The 20th Austrochip Workshop on Microelectronics will be held in Graz on 10 October 2012 at Graz University of Technology.

The conference fee includes conference proceedings, coffee/tea and non-alcoholic drinks at the coffee breaks and lunch (incl. drinks).

Contact

If you have any questions, contact the local organization.

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